



**Shyla N**

**Assistant Professor**

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### **Experience (Teaching and Industry)**

RNSIT (2022- till date)  
FET-Jain University (2019-2022)  
JVIT (2017-2019)  
VKIT (2014-2015)  
GCEM (2011-2013)  
TARINI Embedded Solutions (2009-2010)  
MVJCE (2006-2008)

### **Qualifications**

B.E. From Bahubali College of Engineering, Shravanabelagola.  
M.Tech. N.I.E Mysore.  
PHD (pursuing) from SRM IST, Chennai.

### **Specialization (Academics)**

Signal and Image Processing  
Computer Networks  
Cryptography and Network Security  
Embedded Systems

### **Specialization (Research)**

Signal and Image Processing  
Cryptography and Network Security

### **Publications**

<https://link.springer.com/article/10.1007/s11760-024-03345-4>  
<https://ijece.iaescore.com/index.php/IJECE/article/view/32949>  
[https://papers.ssrn.com/sol3/papers.cfm?abstract\\_id=4527414](https://papers.ssrn.com/sol3/papers.cfm?abstract_id=4527414)  
<https://ieeexplore.ieee.org/document/9792820>  
[https://cdn.techscience.cn/uploads/attached/file/20220207/20220207062629\\_67637.pdf](https://cdn.techscience.cn/uploads/attached/file/20220207/20220207062629_67637.pdf)