

Vision:

To achieve excellence in the area of VLSI design for circuits and systems.

Mission:

-) To induce creative thinking amongst all stake holders.
-) To provide frame work for collaborative and multidisciplinary research.

Objectives:

-) To provide state-of-the-art resources that contribute to achieve excellence in teaching-learning, research and development activities
-) To bridge the gap between industry and academia through MOU and tie-ups.
-) To promote entrepreneurship through innovation to address social issues.
-) To provide suitable forums to enhance the creative talents of students and faculty members.
-) To inculcate ethics and moral values among the faculty and students.

Activities carried out under Center of Excellence in VLSI

) Inauguration of Center of Excellence

Department of Electronics and Communication Engineering, RNS Institute of Technology, Bengaluru initiated the center of Excellence in VLSI in the month of May. **Mr. Harikrishna Rai**, System Architect, Philips Medical Systems inaugurated CoE in Signal Processing on 04/05/2018 and delivered a keynote address on **'Image and Video Analytics'**.



) Expert Lectures:

Mr. Arun John Mathais CoreEL Technologies Bangalore	The Semiconductor Industry	VLSI	8/3/2018
Mr. Chitrananda Sindhu, Technical program manager, INTEL Technologies	Current trends in VLSI	VLSI	10/03/2018
Mr. Pramodh Notiyath, Manager Corporate Application, Synopsys	Smartness is Everything	VLSI	4/5/2018

) Advanced courses in VLSI:

Advanced VLSI course is conducted by Kanada Technologies on weekends (Saturday and Sunday) and holidays. 20 students of ECE dept have registered for this course and took benefit of the 200 hour course. The contents of course is introduction of MOSFET, tool introduction, custom Layouts, standard layouts, memory layouts and advanced topics are Short channel effects, STI, FINFET, Double Patterning, EUV.

) Students projects in VLSI:

Students	Title of the Project	Guides
Mythri R Jessica Ashel William S Sharmeela Kanna	"FPGA BASED DESIGN OF AREA EFFICIENT ROUTER ARCHITECTURE FOR NETWORK ON CHIP (NoC)"	V K Manjula
Neha Mundra Kritika Sharma Abhishek Singh Vandana Kumari	"DESIGN AND IMPLEMENTATION OF 6T AND 12T SRAM CELL WITH DATA AWARE WRITE ASSIST"	Manjunath Naik
Pratiek N Nihal D Rajput S Sawan Kishore Pranava S Manjalagiri	"FPGA IMPLEMENTATION OF CANNY EDGE DETECTION ALGORITHM USING ADOPTIVE THRESHOLD TECHNIQUE"	Dr. Suresh D
Saranya S Meghana D Swetha T D	"PERFORMANCE COMPARISON OF MEMRISTOR BASED MEMORY WITH 6T AND 10T"	Ohileshwari M O
Neeraj S Pai Sharath Kumar R Santhosh Kumar S Rajesh M K	"DESIGN OF PASSIVE PHOTONIC CRYSTAL BASED OPTICAL DEVICES FOR MICROWAVE PHOTONICS APPLICATIONS"	Sreenivasa Babu
Shwetha K S Sowmya C J Vaaniha D H Vinodhini S	"DESIGN AND ANALYSIS OF COMPARATORS USING 180nm TECHNOLOGY"	Dr. Sangeetha B G

Nidhi C S	DESIGN, SIMULATION AND MODELLING OF FBG FOR SENSING APPLICATIONS	Ibrar Jahan
Bindu M		